S/N 09/810,005

IN THE UNITED STATES PATENT AND TRADERIARY OFFICE

Applicant:

Zhongze Wang et al.

Examiner Samuel M2 Gebremariam

Serial No.:

09/810,005

Group Art Unit; 281200M

Filed:

March 16, 2001

Docket: 303.747US1

Title:

METHOD TO REDUCE TRANSISTOR CHANNEL LENGTH USING SDOX

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents Washington, D.C. 20231

3-13-02 T. Flave

PATENT

In response to the Restriction Requirement mailed January 4, 2002, Applicant elects, without traverse, Group I (claims 1-48 and 54). Applicant respectfully cancels remaining claims 49-53 (Group II) without prejudice, and reserves the right to reintroduce them in a divisional application at a later date.

The Examiner is invited to contact Applicant's Representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

ZHONGZE WANG ET AL.

By their Representatives,

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Date 2-9-07

David C. Peterson

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 4th day of February, 2002.

Amy morialty

Signature